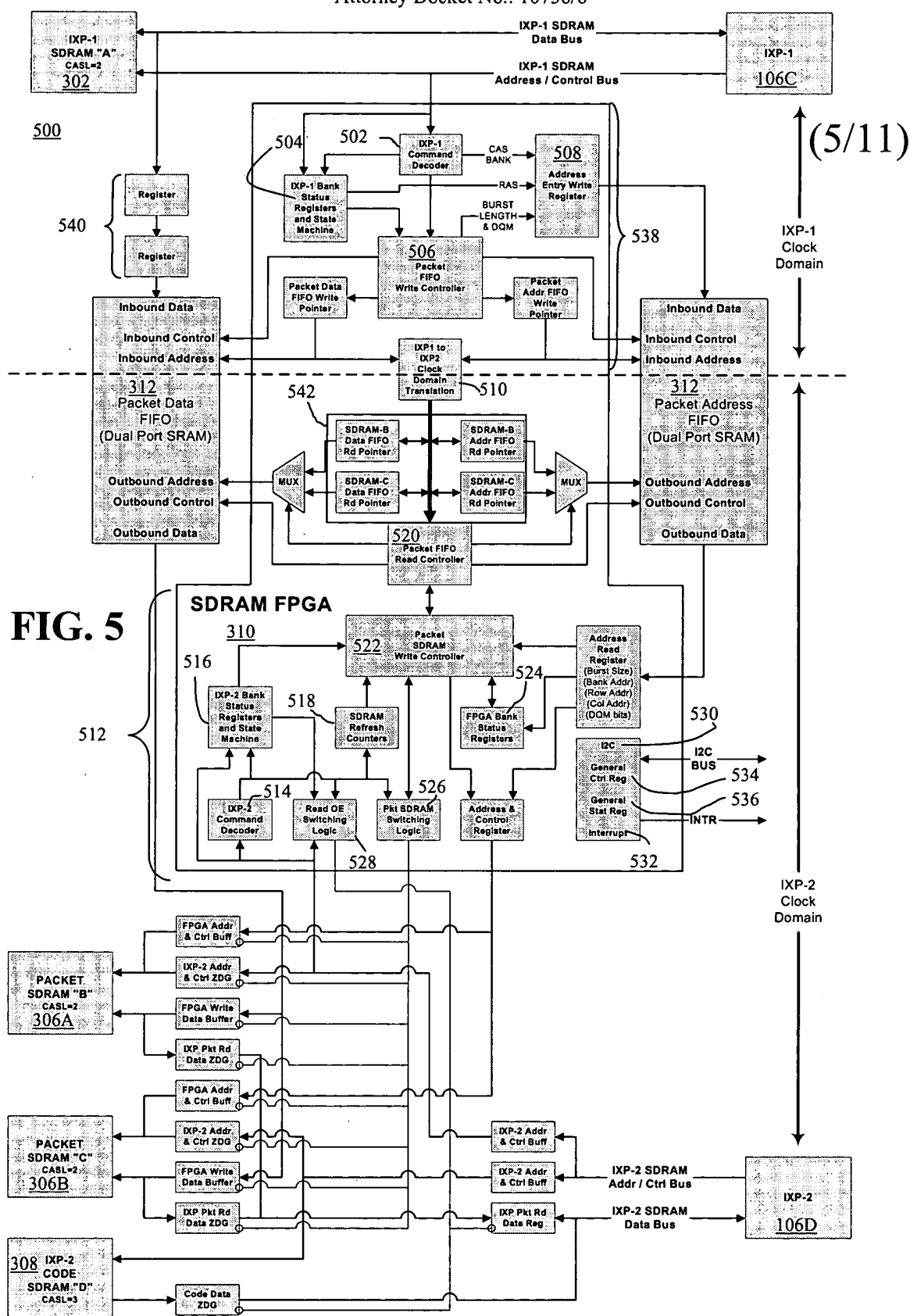


**FIG. 3**

Patent Application For: APPARATUS AND METHOD FOR INTERFACING WITH A HIGH SPEED BI-DIRECTIONAL NETWORK

Attorney Docket No.: 10736/8



Approved by  
11/12/04

# REPLACEMENT SHEET

Patent Application For: APPARATUS AND METHOD FOR INTERFACING WITH A HIGH SPEED BI-DIRECTIONAL NETWORK

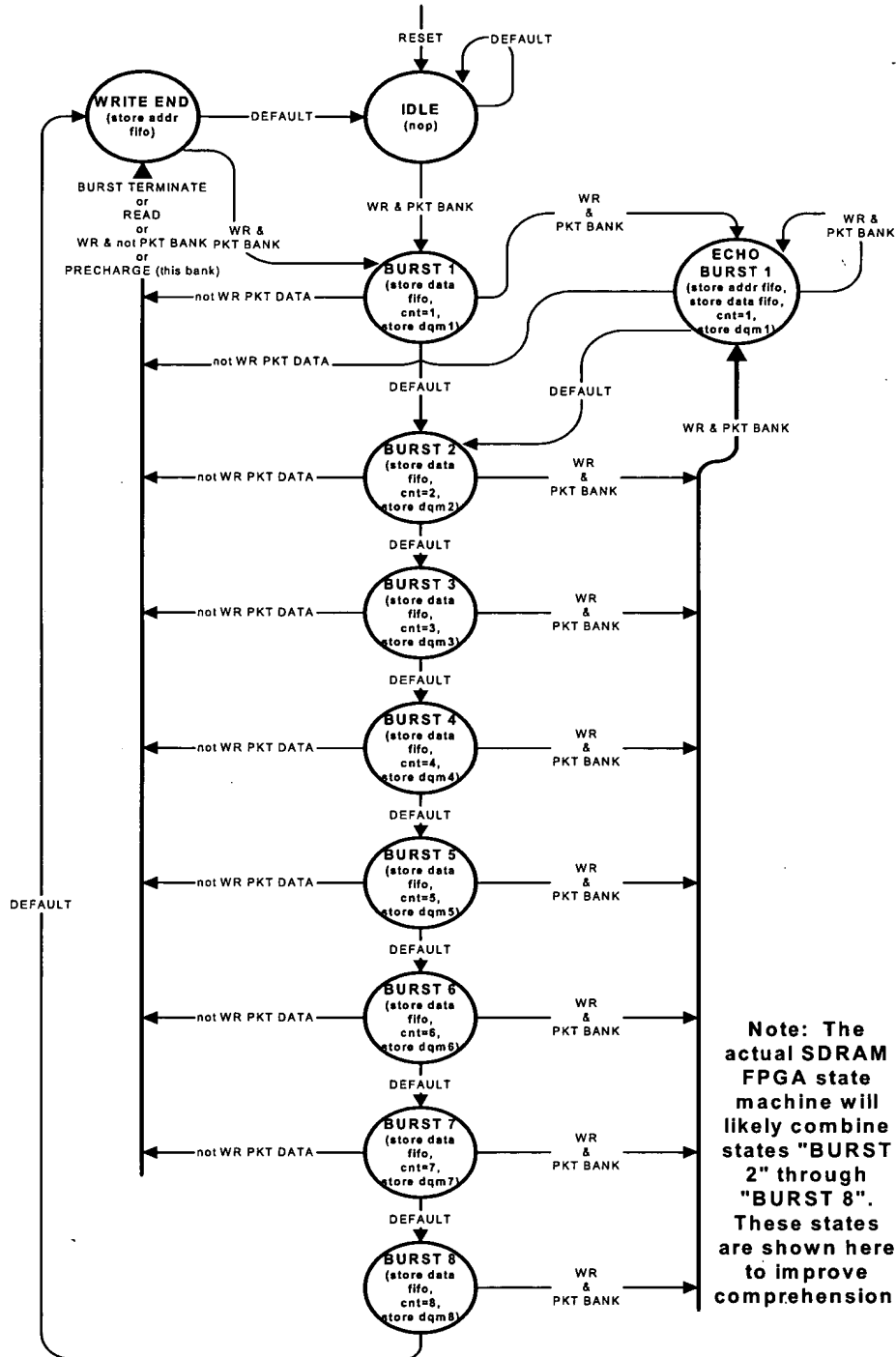
Inventor(s): Zahid Najam, Peder J. Jungck, Macduy T. Vu, Andrew T. Nguyen

Attorney Docket No.: 10736/8

FIG. 7

(7/11)

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**Note:** The actual SDRAM FPGA state machine will likely combine states "BURST 2" through "BURST 8". These states are shown here to improve comprehension.

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11/12/04  
CSC